

CLAIMS

What is claimed is:

1. A system, comprising:

a high-voltage device;

a memory controller having a differential input receiver coupled to an output of the high-voltage device, wherein the differential input receiver is coupled to maintain a voltage level on at least one gate in the differential input receiver at or below a predetermined value;

a reference voltage input coupled to the differential input receiver, wherein the predetermined level is based on the value of a reference voltage on to the reference voltage input; and

an input/output (I/O) controller coupled to the memory controller.

2. The system of claim 1, wherein the high-voltage device is a memory.

3. The system of claim 2, further comprising a processor coupled to the memory controller.

4. The system of claim 2, further comprising a graphics interface coupled to the memory controller.

5. The system of claim 2, further comprising an input/output (I/O) controller coupled to the memory controller.
6. A system, comprising:
 - a high-voltage memory device;
 - a memory controller having a differential input receiver coupled to an output of the high-voltage device, wherein the differential input receiver includes circuitry to maintain a voltage level on at least one gate in the differential input receiver at or below a predetermined value;
 - a processor coupled to the memory controller; and
 - an input/output (I/O) controller coupled to the memory controller.
7. The system of claim 6, further comprising a graphics interface coupled to the memory controller.
8. The system of claim 6, further comprising an input/output (I/O) controller coupled to the memory controller.